

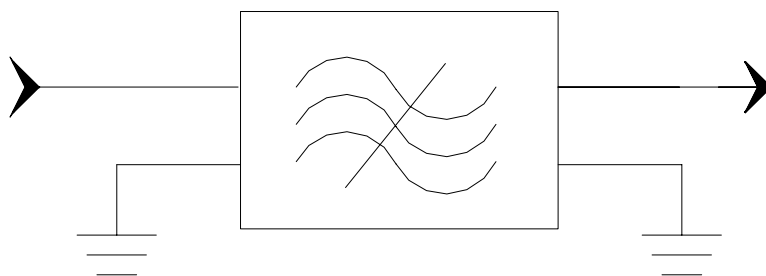
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	55.12	55.2	55.28
Insertion Loss	dB		22.5	24
1dB Bandwidth	MHz	7.1	7.19	
3dB Bandwidth	MHz	7.4	7.7	
40dB Bandwidth	MHz		9.44	9.7
Passband Variation	dB		0.7	1
Absolute Delay	usec		2.2	2.3
Group Delay Variation($f_0 \pm 2.84\text{MHz}$)	nsec		110	150
Phase Linearity($f_0 \pm 2.84\text{MHz}$)	Usec		5	6
Ultimate Rejection	dB	55	60	
Substrate Material		128LN		
Ambient Temperature	°C	25		
Package Size	DIP2712 (27.0x12.8x4.7mm ³)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



Source/Load Impedance=50 ohm

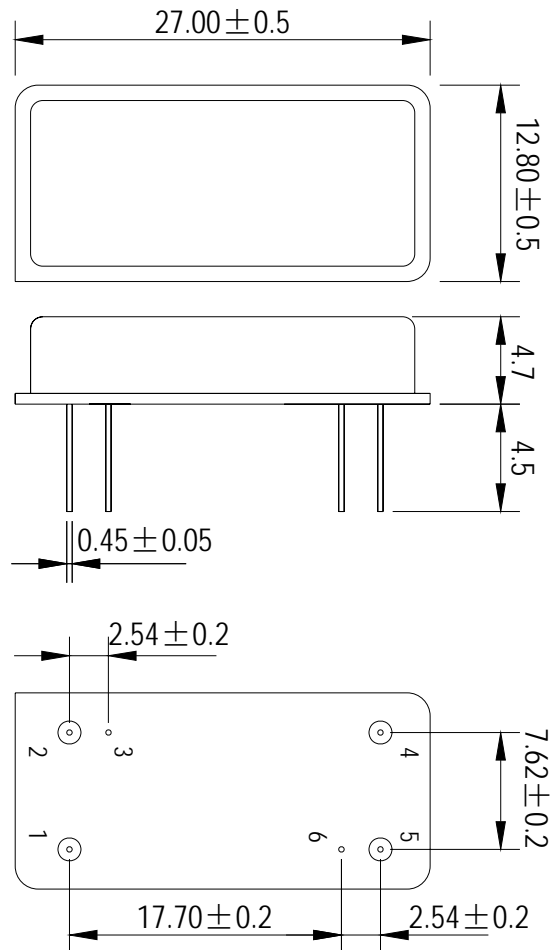
Notes - Component values may change depending
on board layout.



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

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Package Dimension



Pin 1: input
Pin 5: output
Others: Grounded

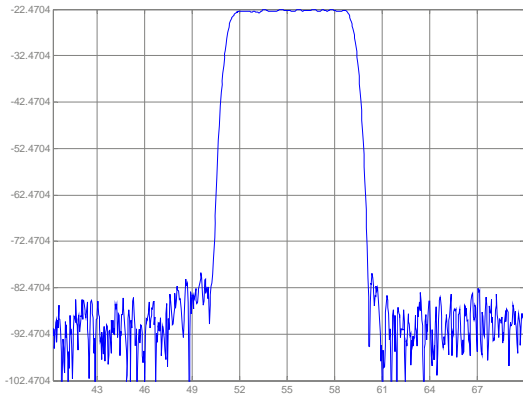


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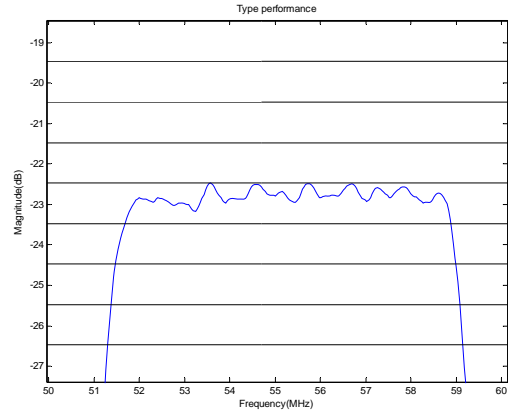
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Typical Performance

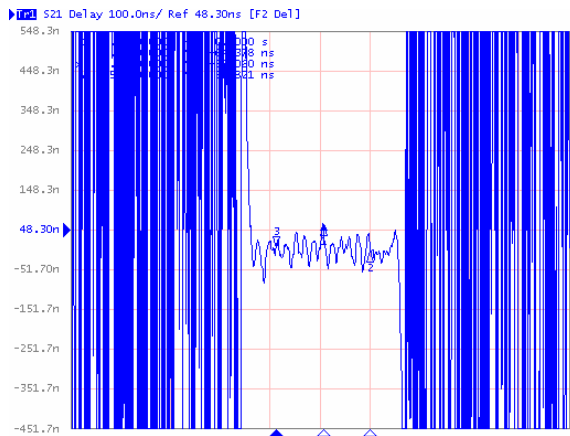
Frequency Respond



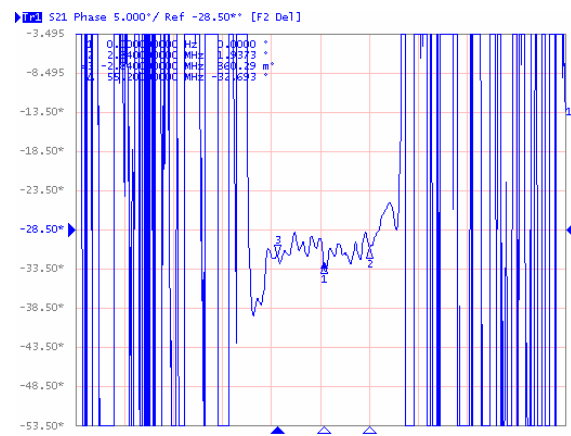
Passband Respond



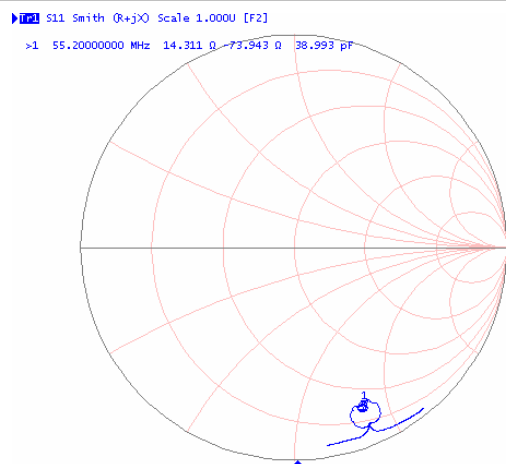
Group Delay Variation($f_0 \pm 2.84\text{MHz}$)



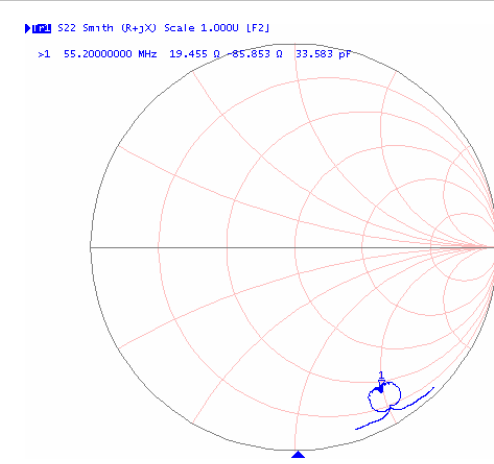
Phase Linearity($f_0 \pm 2.84\text{MHz}$)



Simth Chart S11



Simth Chart S22



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